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Atmel 8051 Microcontrollers Hardware Manual 1-4 4316E-8051-01/07 1.3 Arithmetic Instructions The menu of arithmetic instructions is listed in Table 1-2. The table indicates the addressing modes that can be used with each instruction to access the operand. For example, the ADD A, instruction can be written as:

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Atmel 8051 Microcontrollers Hardware Manual 1-1 Rev. 4316A-8051-01/04 Section 1 The 8051 Instruction Set The 8051 instruction set is optimized for 8-bit control applications. It provides a variety of fast addressing modes for accessing the internal RAM to facilitate byte operations on small data structures.

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Atmel 8051 Microcontrollers Hardware Manual Common Features Description When the CPU is executing out of external Program Memory, all 8 bits of Port 2 are dedicated to an output function and may not be used for general purpose I/O.

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Atmel 8051 Microcontrollers Hardware Manual 1-4 4316C-8051-05/04 1.2.3 Register Instructions The register banks, containing registers R0 through R7, can be accessed by certain instructions which carry a 3-bit register specification within the opcode of the instruction.

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1IntroductionThe C51/C251 Demo Board allows easy evaluation of most of Atmel C51/C251 devices.The C51/C251 Demo Board can be powered using a simple ...

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C51: ACCESSING INTERNAL EEPROM ON ATMEL 89S9252

QUESTION: I was looking for information concerning writing to and reading from the 2K EEPROM space of the AT89LS8252. Does the Keil C language directly support this? Are any examples available? ANSWER: To access AT89S9252's internal EEPROM, you must drive a flag in a new special function register...